



Attorney Docket No.

032840-008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Ted JOHANSSON et al.

Application No.: 09/500,944

Filing Date: February 9, 2000

Title: Semiconductor Device with Deep Substrate Contact

**Non-Fee Amendment**

Group Art Unit: 2826

Examiner: Victor A. Mandala

Confirmation No.: 3285

**AMENDMENT/REPLY TRANSMITTAL LETTER**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Enclosed is a reply for the above-identified patent application.

- ☐ A Petition for Extension of Time is also enclosed.
- ☐ A Terminal Disclaimer and a check for ☐ \$55.00 (2814) ☐ \$110.00 (1814) to cover the requisite Government fee are also enclosed.
- ☒ Also enclosed is/are a Request for Approval of Drawing Changes.
- ☐ Small entity status is hereby claimed.
- ☐ Applicant(s) requests continued examination under 37 C.F.R. § 1.114 and enclose the ☐ \$370.00 (2801) ☐ \$740.00 (1801) fee due under 37 C.F.R. § 1.17(e).
- ☐ Applicant(s) previously submitted \_\_\_\_\_ on \_\_\_\_\_, for which continued examination is requested.
- ☐ Applicant(s) requests suspension of action by the Office until at least \_\_\_\_\_, which does not exceed three months from the filing of this RCE, in accordance with 37 C.F.R. § 1.103(c). The required fee under 37 C.F.R. § 1.17(i) is enclosed.
- ☐ A Request for Entry and Consideration of Submission under 37 C.F.R. § 1.129(a) (146/246) is also enclosed.
- ☒ No additional claim fee is required.

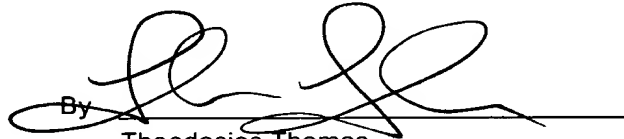
The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17, 1.20(d) and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.


P.O. Box 1404  
Alexandria, Virginia 22313-1404  
(919) 941-9240

Date: October 17, 2002

By   
Theodosios Thomas  
Registration No. 45,159

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: October 17, 2002

  
Jennie Snead



Patent

Attorney's Docket No. 032840-003

#9  
Amalt  
10/28/02  
CUT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Ted JOHANSSON et al.

Application No.: 09/500,944

Filed: February 9, 2000

For: Semiconductor Device with Deep  
Substrate Contact

Group Art Unit: 2826

Examiner: Victor A. Mandala

Confirmation No.: 3285

RECEIVED  
OCT 24 2002  
TECHNOLOGY CENTER 2800

AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Examiner's Action mailed on July 17, 2002, please amend this application as follows.

In the Specification

Please **ADD** a paragraph beginning at page 5, line 12, as follows:

c1 Fig. 4 shows a partial cross section of a semiconductor circuit, comprising a double MOS-transistor with a ground connection according to the present invention.

Please **REPLACE** the paragraph beginning at page 8, line 23, with the following:

c2 Figures 1 and 2a-2e only described a NPN bipolar transistor. It will be understood by one of ordinary skill in the art that other types of semiconductor devices, such as PNP bipolar transistors, MOS transistors or discrete components, may be implemented having a ground connection being connected to the reverse side of the substrate as shown in Fig. 1. For example, a MOS transistor according to